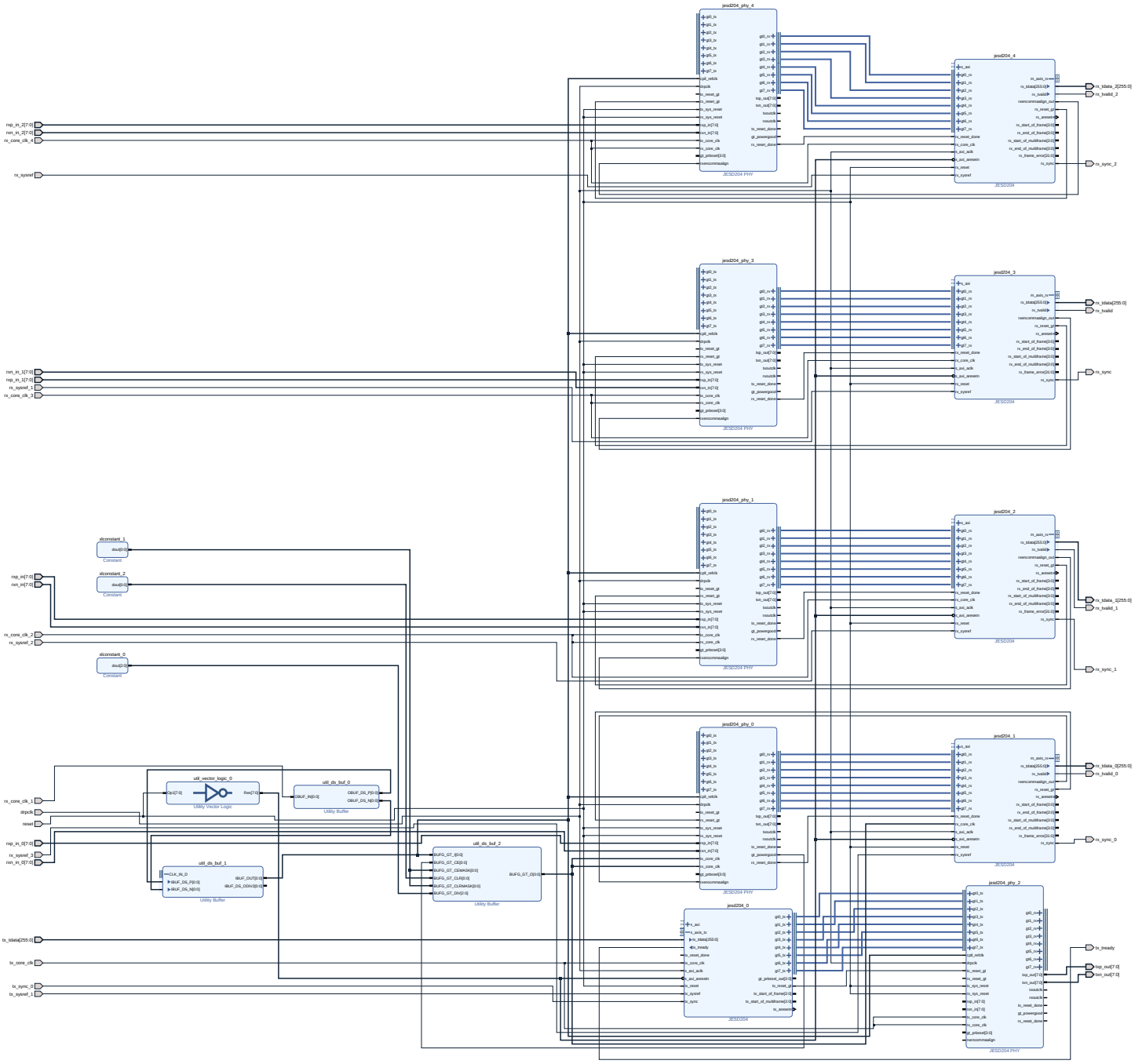
jesd204_phy_4
JESD204 PHY
jesd204_4
JESD204
rxp_in_2[7:0]
rxn_in_2[7:0]
rx_core_clk_4
rx_sysref
rx_tdata_2[255:0]
rx_tvalid_2
rx_sync_2
jesd204_phy_3
JESD204 PHY
jesd204_3
JESD204
rxn_in_1[7:0]
rxp_in_1[7:0]
rx_sysref_1
rx_core_clk_3
rx_tdata_1[255:0]
rx_tvalid
rx_sync
jesd204_phy_1
JESD204 PHY
jesd204_2
JESD204
xlconstant_1
Constant
xlconstant_2
Constant
xlconstant_0
Constant
rxp_in[7:0]
rxn_in[7:0]
rx_core_clk_2
rx_sysref_2
rx_tdata_3[255:0]
rx_tvalid_1
rx_sync_1
jesd204_phy_0
JESD204 PHY
jesd204_1
JESD204
util_vector_logic_0
Utility Vector Logic
util_ds_buf_0
Utility Buffer
util_ds_buf_1
Utility Buffer
util_ds_buf_2
Utility Buffer
rx_core_clk_1
drpclk
reset
rxp_in_0[7:0]
rx_sysref_3
rxn_in_0[7:0]
rx_tdata_0[255:0]
rx_tvalid_0
rx_sync_0
jesd204_0
JESD204
jesd204_phy_2
JESD204 PHY
tx_tdata[255:0]
tx_core_clk
tx_sync_0
tx_sysref_1
tx_tready
txp_out[7:0]
txn_out[7:0]